# PEEK (65)

The Unofficial OSI Users Journal

P.O. Box 347 Owings Mills, Md. 21117 (301) 363-3267 Editor: A! Peabody Vol. 2, No.8 Aug. 1981

## Column One

In this month's PEEK(65), you will see a couple of letters which are answered by 'Dick' and 'Brian.' Dick, of course, is Dick McGuire, our technical editor, the same fellow who writes Tech Notes from time to time, when he has time and something interesting to share with us. Brian is Brian Hartson, a hardware— oriented type who is a great help to all of us around here.

All this brings up once more the fact that PEEK(65) is no longer a one- or two-man effort. There are at least 5 of us who work on it regularly, with occasional help from still more. I hope that we will be able to change our credit box in the next month or two to reflect some of the hard work which these nice people put into PEEK(65) each month.

This month's issue continues our series on the CP/M connection with a Microspell review. I am very excited about this series, and you should be too, whether you have a C3 or not. If you have a C3, of course you are excited that you can now run CP/M with the wealth of software written for it. But even if you have a C1P, the CP/M connection has meaning for you. How so? Like so...

A year or two back, we had 65D. Other folks had their Appledos or Cdos or Trsdos; most folks had Nodos, because most folks had no disk to dos. Now, lots of us have 65U, increasing numbers of us have CP/M, and all of us are peering into the not too hazy future to see what is coming

along. And it looks good. New 16-bit machines. New user-friendly operating systems. New languages. New applications software and games and lots more, increasingly portable from machine to machine and even level to level (Minito Micro, even Mainframe to Micro).

Still won-Still unmoved? dering what that has to do with your personal computer with no disk? OK, let's think which house to the strink about it a little more. What kind of car do you drive? What kind of car did you drive 5 years ago? In most cases, the answers to those two questions are not the same. Point is, we do change equipment, we do update our systems, swap 'em in on newer and better ones; so it will pay you to keep your eye on what is coming around the bend, to take an interest when somebody from the factory says the new generation of OSI machines will start off with a 5 MByte 'mini-winnie' 5.25' 5 MByte mini-williams broadess to 30 Megabytes on line in the same space in a few years. significant to you that these machines will run CP/M, MP/M, Oasis, 65U, 65D and who knows just what all.

Even if you never swap your good old faithful ClP, there will be new expansion boards, alternate memories, new systems of software; it all trickles (or maybe pours) down. This issue also has a mention of the use of non- OSI boards in a reader's system. 'Hey, self,' I said to myself when I read that, 'you mean we can use other folks' boards in

this machine?' Obviously, we can. Just ponder a moment the significance of that bit of intelligence, both for now and more particularly for next year. I can see it now, the C1P to S-100 adapter, cheapo model for I/O and RAM boards only (which already gives you a few dozen to choose from), deluxe model for \$10 more to cover the cost of the Z-80 it carries to allow you to hook up ANY S-100 board known to man to your SII.

Of course, there will be more to it than that, limitations and problems I haven't even thought of, and let me make it perfectly clear that I have no inside dope on any such adapter, factory or otherwise. I am just dreaming. Same way I used to dream, way back 10 months or so ago, about a desktop computer with a hard disk for less than a Porsche.

The PEEK(65) CBBS has fallen on hard times. We have been plagued with pesky software and hardware problems (which, as usual, I believe are now solved), causing some good PEEKers to waste their hard-earned nickels on phone bills when the system answered the phone, but then wouldn't work with them. Suffice it to say it is back up now, running fine, with several new commands (maybe our continual expansion and improvement has something to do with the problems) and ready for your messages. Faithful users, thanks. New users, welcome. Non-users, give us a call! Computing by phone is fun and easy.

THE CP/M CONNECTION

\*\* a MICROSPELL review \*\*

Automatic Spelling Aid

by Al Peabody

Last month, in our first CP/M product review, we had a brief look at the standard which many people use to judge word processing programs, WordStar, and we were indeed impressed. Since then, we have been using WordStar quite a bit, and nothing we have seen has changed our initial high opinion of it. It does have a few problems, as we mentioned last month, but they are minor and, in our opinion, more than compensated by its low price (compared to a dedicated word processor) and ease of use. Familiarity has bred increased respect.

This month, we would like to have a look at another phenomenon, and one representative of the phenomenon in particular. The phenomenon is the proliferation of companion programs designed to work with WordStar, both programs written by MicroPro and those of other venders. WordStar is coming to be an entire system of interacting software, not just a nice program.

The particular member of the WordStar family with which this review is concerned is called Microspell. As the name indicates, Microspell is intended to turn your micro into a spelling machine. The way it does this is quite interesting.

Microspell does not just look through your WordStar files and compare every word with some giant dictionary looking for misspellings: that would require a dictionary far too

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large for practical use. Instead, Microspell uses 4 dicdionaries, each for a certain section of the alphabet (LEX1, for example, covers words beginning with A through D). Furthermore, Microspell is "intelligent" enough to recognize that "covers" is "cover" plus "s", that "denies" is "deny" - "y" + "ies," and so forth. This intelligence greatly expands the effective size of the dictionary and the capability of the program.

When Microspell encounters a word it thinks it recognizes by applying rules like these, it prints out its guesses, such as:

DENY - Y + IES

and proceeds. When it stumbles across a word it does not recognize, but which is fairly close to one or more words it DOES recognize, the display looks something like:

PROCEAD: I GUESS:

1) PROCEED

2) RECEDE

and all you have to do is tell it which guess is right, or of course type in the correct word if none of the guesses is right.

Finally, Microspell may run into a word in your WordStar file which it just doesn't recognize at all. Then it prints out the word, and asks you to decide whether it is right or wrong. If it is right, you can type in a control code and it will be recognized as correct for the rest of the document, making it unnecessary to tell it over and over again. You can even insert the new word into the permanent dictionary, though that uses up disk space, and slows down the operation of the program; so you wouldn't want to do that for words not likely to occur in other documents.

How well does Microspell work, and how does it feel to use it? To find out, I tried it out on the file containing last month's WordStar review. All I had to type was

A> SPELL B: WordStarRVU.DOC

and we were off. A confusing array of guesses as to plurals and indirect forms of words flashed on the screen, but I soon got used to them, and was glad I didn't have to tell Microspell that "awaited" was the past tense of "await." Then we got to the first time

"WordStar" came up, and it wasn't in the dictionary. I decided to accept it. Then came CP/M, then OSI, then 65U, etc., etc. Actually, all the A-D words came up first, followed by the E-J words, then the rest in two more passes. I guess it went through the 2-column review faster than I could have proofed it myself, and I had the security of knowing every single word was spelled correctly...

made me proof it again, and sure enough, there were still some mistakes! But only the sneaky ones. Like when I typed "in" when I should have typed "is."

Unfortunately, in my particular case, it turns out that these are my most common sort of mistake. Between them, and the tendency of Microspell to make unwarranted assumptions (blindly following its rules to make plurals out of words which were just plain typos), I found I had to proof my work pretty carefully, anyway. And still doubtless missed some mistakes!

Conclusions? Draw your own. Microspell is not very expensive, and while it is not blindingly fast, it is quicker than I can read carefully. It does require that you watch it carefully, being sure its guesses are right (that's why it prints them out for you... the instructions tell you to watch closely!) And it will catch plain old typos like "teh" and "adn" just about every time. So if you are a member of the "fatfinger" school of typists, but need to turn out beautiful work, Microspell might very well help you do so. It's worth a try just to read the very complete and well-written manual!

Next month -- CP/M itself.



INDIRECT JUMPS FOR THE C2/C4

submitted by: A. Penaloza 9105 Cherry Avenue Morton Grove, IL 60053

OVERVIEW

The C2/C4 system monitor (SYNMON Ver 1.0) on the 502 board has absolute jumps to the BASIC input, output, control c, save and load routines. If you have a disassembly, these jumps are seen in memory \$FFEB to \$FFE9. In the C1. these are indirect jumps.

## GOODIES for 051 Users!

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,	'	need to be a C1P or SII Wizard, just	•	\$7.95 \$	···

The Cl monitor transfers these jump addresses from ROM into RAM, whenever the break key is pressed. This gives the Cl users an easy way to tie in their special functions, i.e. cursor control. All they need to do is to change the appropriate jump addresses, that had been moved to RAM, to point to their new routines.

However, C2 users are faced with a problem. The SYNMON does not transfer these jump addresses to RAM, but instead calls them from ROM when needed. This makes C2 users despair at the thought of adding new functions, e.g., BASIC Shorthand. Additions cannot be done easily, since ROM contents cannot be changed.

C2/C4 users can, however, now easily add fancy functions or better yet, use already published software written for the Cls. The solution is to improve the monitor by reprogramming it into a 2716 EPROM.

A 2716 EPROM is similar to a 2316B SYNMON but for 2 pins, 18 and 20. The 2716 contains 2048 bytes of memory. Therefore, its address range is from \$0000 to \$07FF. The C2 monitor routines, for non disk systems, are in addresses \$0200 to \$04FF of the SYNMON chip. The rest of SYNMON contains the bootstrap for disk systems. This is why SYNMON is not directly addressed by non-disk systems. The address \$FD00 is really SYNMON address \$0200. So, when the improved monitor is programmed, make sure to put it at addresses \$0200 to \$04FF of the 2716. Otherwise, the new monitor will not work.

### SOFTWARE

The following software changes will give the C2 user the indirect jumps of the C1, USR(X) callable screen clear and move the IRQ vector out of BASIC's stack.

There are three sections to be changed. The first at addresses \$FE00 to \$FE29 (screen clear). The second at addresses \$FF00 to \$FF37 (screen The third at ad-\$FFEB to \$FFFF clear). dresses (absolute jumps).

Thanks must go to David Jones for his modification of changing an in-line screen clear into a subroutine (Peek (65), Jan. '81). His change wipes out the initialization

#### TABLE 1

	OLD CODE					NEW COD	E	
(FE00)	4302	A228 9A D8				9A	LDX TXS CLD	#\$28
÷	4304 4307	AD06FB A9FF	LDA LDA	\$FB06 #\$FF \$FB05	4304 4307	200EFE 84FF	JSR STY	\$FEOE \$FF \$FB
	430C 430E	A2D8 A9D0	LDX LDA	#\$D8 #\$D0	430B 430E	4C43FE 48	JMP PHA	\$FE43
	4312	85FF A900 85FE	LDA	#\$00	4311	A208 A000 84FE	LDY	#\$08 #\$00 \$FE
	4318	85FB A8 A920	TAY		4317	A9D0 85FF A920	STA	#\$D0 \$FF #\$20
	431B 431D	91FE C8	STA INY	(\$FE),Y	431B 431D	91FE C8	STA INY	(\$FE),Y
	4320 4322	E6FF E4FF	INC CPX	\$431B \$FF \$FF	4320 4322	E6FF CA	BNE INC DEX	
	4326	84FF	STY	\$431B \$FF \$4343	4325	68	BNE PLA RTS	\$431B
	432A		JSR	\$FEE9	4327	FF FF	333	
(FE2D)						20E9FE C92F	JSR CMP	

#### TABLE 2

OLD CODE					NEW CODE			
(FF1B)	4401 4403 4407 4407 4402 4415 4412 4412 4412 4423 4422 4423 4423 4423	A228 9A 2022BF A000 8C1202 8C0502 8C0502 ADE0FF 8D0002 A920 A920 9900D7 9900D4 9900D3 9900D1 9900D0	TXS JSR LDY STY STY STA LDA STA STA STA STA STA STA STA STA STA	#\$28 #\$00 \$0212 \$0203 \$0205 \$0206 \$FFE0 \$0200 #\$20 \$D700, Y \$D600, Y \$D500, Y \$D400, Y \$D300, Y \$D300, Y \$D300, Y	4407 4407 4407 4407 4415 4415 4411 4425 4425 4422 4422 4422	A228 9A 2022BF A000 8C1202 8C0502 8C0502 ADE0FF 8D000FE BD2BFF 9D1802 E8 E00A D0F5 FF 67 FF 99FF89 FF 94FF	TXS JSR LDY STY STY STY LDA STR LDA STNX EPX BEQ C????	#\$00 \$0212 \$0203 \$0205 \$0200 \$FE0E \$FF2B,X \$0218,X #\$0A \$441E #4438
(FF3B) (FFEB)	443B 44EB 44EE 44F1 44F4 44F7	F006 4CB8FF 4C67FF 4C99FF 4C89FF 4C94FF 3001 00 FF	BEQ JMP JMP JMP JMP BMI BRK ???	\$FF67 \$FF99 \$FF89 \$FF94 \$44FD	443B 44EB 44EE 44F1 44F4	FF B95FFF F006 6C1802 6C1A02 6C1C02 6C1E02 6C2002 3001 00 FF 0F	BEQ JMP JMP JMP JMP	(\$0218) (\$021A) (\$021C) (\$021E) (\$0220)

for the old 430 board, but users without this board are unaffected.

The screen clear at \$FE00 is replaced by Jones' mod. The second screen clear at \$FF00 is replaced with a JSR to Jones' mod., a routine to transfer the jump address to RAM and the address table. The absolute jumps at \$FFEB are replaced by indirect jumps like the Cl's.

### PROCEDURE:

- 1. Use the extended monitor to move the contents of SYNMON (from \$FD00 to \$FFFF) into any open RAM space. I used \$4200 to \$44FF. Remember that the new monitor must occupy EPROM addresses \$0200 to \$04FF.
- 2. Make the changes as shown in the tables.
- 3. Program into a 2716 EPROM.

### HARDWARE

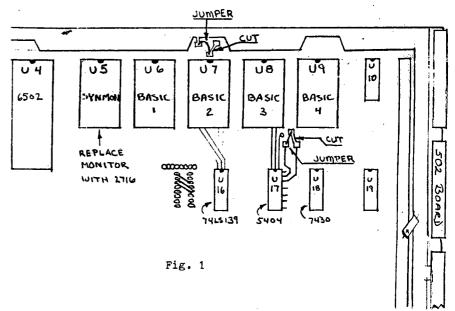
To make the monitor socket capable of accepting the 2716, two pin connections (18 & 20) need to be cut and rejumpered. Jumper pads are provided on the 502 board for this purpose. (NOTE: this will void any existing warranty.)

### PROCEDURE:

- 1. Unplug computer, remove 502 board.
- Position board as shown in fig. 1. Locate SYNMON ROM and BASIC 2 ROM.
- Locate jumper pads at top of BASIC 2 ROM.
- 4. Cut foil and solder a jumper as shown. (This takes care of pin 18.)
- 5. Locate jumper pads at top of Ul7 (5404), between and beneath BASIC 3 and 4.
- Cut foil and solder a jumper as shown. (This takes care of pin 20.)
- 7. Check your work. Make sure the foils are open where cut and connected where jumpered.
- 8. Replace SYNMON with the improved 2716 monitor and power up.

### CONCLUSION

You now own a unique hybrid. The C2/C4 is still compatible with its own series and is easily able to use similar special routines that use the C1 vectors (input, output,



ctrl/c, save and load). In addition, you now also have a USR(X) callable screen clear routine, and an IRQ vector at \$020F to \$0211. Look into back issues of MICRO and AARDVARK JOURNAL for fancy functions, that use the ClP vectors, to use with your new C2/C4.

For those who would like all of the above, but do not have access to an EPROM programmer, I will program one for you at a cost of \$15.00. This includes cost of EPROM, mailing and handling. Please allow four weeks for delivery.

TECH NOTES by Dick McGuire

First, there have been several requests to modify RESEQ to work on a hard disk. The author, Jim Sanders, has kindly worked these out for me and I'll pass them on to you at this time.

Before we start you must understand that there are several versions of RESEQ, RESEQ5, RESE around and they are all numbered differently so I will describe the approximate position of the lines to be changed and will give you the old line and the change to be made.

First create a new slave system to hold your RESEQ files and copy the files from your RESEQ disk to the hard disk. Then load RESEQ and look toward the end of the program for the line

CLOSE: DEV"A": OPEN "RES\*D\*",1: CLOSE: RETURN

delete this line while noting its line number. Then look in the first 10-15 lines to find the line containing the instruction

FLAG9:GOSUBnnnn:FLAG10

where nnnn is the number of the line you just deleted. Delete this portion of that line. Then about 30-40 lines from the top of the program look for the line

POKESB,0:POKESB+216,0:POKE SB+217,0:REM

change this line to read

POKESB, 0: POKESB+216, 128: POKESB+217, 128

Save this program back where it came from. Do the same with RESEQ5 and RES6. Your RESEQ will now work on the hard disk. All you have to do to use it is to load the program you want to resequence, POKE 13314 and 13315 to the cylinder address of the RESEQ system and operate as normal.

Second, there have also been several requests to know how to write a program which will allow simultaneous editing of the same file without contention. There are two ways to do this, one is easy but has problems while the other is harder (and slower) but has fewer problems.

The first method is adequately described in OSI's Multi-User

manual and essentially requires locking the entire file for the entire time that a record will be displayed on the screen. The problem with this method is that operator "A" may get a telephone call before finishing and leave the file locked for a long period of time.

The second method is more difficult, but it eliminates that problem. The sequence of events is this: 1) WAIT FOR the resource, 2) OPEN the file, 3) READ the record, 4) MARK the record busy flag (a field in the record busy flag (a field in the record on the disk, 6) CLOSE the file, 7) CLEAR the wait, 8) UPDATE the record in memory, 9) WAIT FOR the resource, 10) OPEN the file, 11) READ the record again being sure not to read the old information in on top of your updated information 12) WRITE the updated information, 13) CLEAR the record busy flag, 14) CLOSE the file, and finally 15) CLEAR the wait again.

The record busy flag is a field in the record which must be tested by the user program to see if the record is busy. If it is not busy the program may proceed, but if the record is busy some option must be made available to the operator to update another record or wait or something. This flag must be written in the record on disk before proceeding. This is assured by the CLOSE in 6.

Once the record has been updated and is ready for writing on to the disk, it must first be read. This is to insure that the copies in RAM of the records on either side of the updated record are the most current. It would be possible otherwise for operator "A" to update record 100 while operator "B" updated record 101. Because the computer reads into its buffer not just the selected record but the entire track within which the selected record resides, one operator could update one record while another operator was updating another record from the same track. When they wrote their updated records back on the disk, the rest of the track would be restored to the condition it was before the last operator to write first Of course it read the disk. is necessary to prevent the second read from overlaying the updated record in RAM. In fact, it is only necessary to read something on the same track.

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"EXTRACTS FROM THE PEEK (65)

\*OHIO SCIENTIFIC SOFTWARE DIRECTORY\*

OSI is forming a software DIR of outside users. If you want your programs listed in this DIR, please fill out the following information and send to the address at the end of the form. My name is Rich Comeau. I am an OSI employee. My Peek (65) CBBS number is 3512.

Product name: Description: Date of First Installation: Number of Installations: As of Date: Application Size Limitations (e.g. number of inventory records, etc.) Features: Capacity: Operating System Needed: OS-DMS Compatibility: Hardware Requirements: RAM in KBYTES: Disk in KBYTES: Terminals Supported: Modification Required to OS: Which System will It Run On: Floppy Disk Single Sided: Double Sided: CD-7: Hard Disk

CD-36: CD-74: Time Sharing (Level 3): Network (Level 3): Items Supplied with Package: 8" Diskette( ) Technical Man() Listing( 5" Diskette( ) End User Man( ) Other() Suggested Retail Price: Dealer Price: Willing to Cross License: Installation Assistance: Installation Manual() Telephone Consultation with Package() Hours: Toll Free() Additional Assistance Available () Rate/Hour: Other: Continuing Support: Bug Fixes() Periodic Updates()

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FBASIC is extremely fast. Allowing you to do unheard of things in BASIC. Things that cannot be done in any other language except assembler, with no need to learn a new language.

For the example program:

10 FOR I=1 TO 60000

20 A=A+1

30 NEXT I

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FBASIC also includes a Crossreference utility which produces a complete sorted list of all line and variable references within a program. The Cross-referencer was written in FBASIC and takes less than a minute on even the largest program (written in OSI BASIC it would take upwards of an hour and a half!).

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\*OSI HIGH RESOLUTION GRAPHICS EXPANDER 541 BOARD\*

The model 541 graphics expander is a standard OSI 48 line bus circuit card which expands the capabilities of OSI's 540 video text and character graphics display to include 256\*512 individual point addressable graphics.

The 541 uses 8K of on board memory to memory map the 256\*512 points. The output of the display is mixed with the 540's output providing extremely versatile and convenient to program displays which have alpha characters, graphics characters, up to 16 colors and High Resolution Point Graphics on one video screen.

The 541 also has a total of 24K 2114 type RAMs which allows it to completely emulate OSI's 527 24K RAM board. When placed in the second 24K segment of the address space of a 48K user partition, the board can be used either totally as a program memory card with the graphics turned off or the lower 8K can be used for program memory (yielding 32K workspace) and the upper 16K for memory mapped graphics.

The 541 is available immediately in specially factory configured C4P and C8P computers. C4PMF-HR & C8PDF-HR

The 54l can be added to existing C4P and C8P computers. However, it requires several modifications. This will only be offered as a factory mod.

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terminals. New terminals supported: DEC VT-100, DEC LA-120, DEC LA-36, DEC LA-34 and TELETYPE 43(TTL)

### \*OSI SERVICE NETWORK\*

Alanthus Data Communications and OSI, Inc. have announced jointly that Alanthus will commence third party maintence on OSI business computers. This capability will assure all OSI customers of nation wide service by placing a highly professional experienced field service organization at the disposal of multi location customers as well as single users.

### \*OSI MULTI-USER Z80 SYSTEM\*

OSI has signed a contract with phase one for the installation and distribution rights to the OASIS operating system. This version of OASIS will be a full multi-user Z80 based system which will run on C3-B and C3-C computers.

## \*OSI DATA COMMUNICATIONS CAPABILITY\*

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### SUPERBOARD II to CARD CAGE EXPANSION

by Alex Jackson 1707 Providence Road Towson, MD 21204

Encouraged by Bruce Showalter's experience (Aug. '80, pg. 9) I decided to build a wire wrap Static Memory card. Using readily available parts (to me) I now have a 16K memory board and buffer interface to the Super-II expansion socket.

These and any future projects will slide into a rack mountable card cage with a wire wrapped back plane.

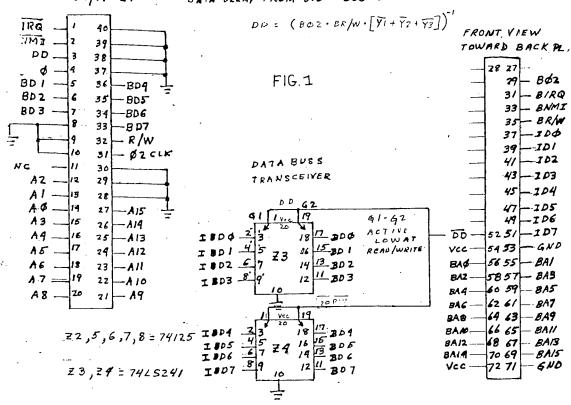
The I/O board is a too small Radio Shack type 244 which buffers all lines. Individual buffering is probably redundant, but acceptable for an early development project such as this. Notice fig. 1, the address lines are always on together with 02 clock, read/write, etc. Whenever memory expansion is addressed data direction (DDX) goes low

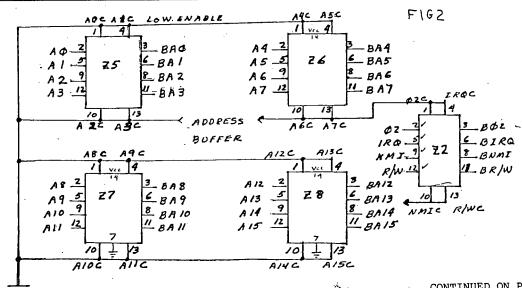
permitting data flow to or from the bus.

The 74LS241 was chosen for availability and cost. As configured it works well with the 600 board's 8T28's.

The memory expansion calls for two 16K memory cards. Addressing is from \$2000 to \$5FFF on the first card and \$6000 to \$9FFF on the second card. So far I've only built MEM-BD-1. Addressing for the second board would use Y3X and

JI/PI-ZY DATA DELAY FROM 610 TO 600 80





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entire disk of data at one time.

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- 1) A tutorial on Machine Code for BASIC programmers.
- 2) Complete listings of two word processors for BASIC IN ROM machines.
  - 3) Moving the Directory off track 12.
- 4) Listings for 20 game programs for the OSI.
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P.S. We're so confident of the quality of these programs that the documentation contains the programmer's home phone number!

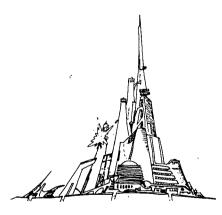
#### SUPERDISK II

This disk contains a new BEXEC\* that boots up with a numbered directory and which allows creation, deletion and renaming of files without calling other programs. It also contains a slight modification to BASIC to allow 14 character file names.

The disk contains a disk manager that contains a disk packer, a hex/dec calculator and several other utilities.

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MINOS - 8K - - Features amazing 3D graphics. You see a maze from the top, the screen blanks, and when it clears, you are in the maze at ground level finding your way through on foot. Realistic enough to cause claustrophobia. — \$12.95

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LABYRINTH - 8K - This has a display background similar to MINOS as the action takes place in a realistic maze seen from ground level. This is, however, a real time monster hunt as you track down and shoot mobile monsters on foot. Checking out and testing this one was the most fun I've had in years! — \$13.95.

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CONTINUED FROM PAGE 8

Y4X generated on the first, see fig. 2.

The first problem arose when generating DDX which was originally inverted.

Noise has been the only obvious remaining problem. Grounding all unused fingers to the back plane has made the system reliable at 1Mhz. The 600 board ran great at 1.3 Mhz. by itself.

The Sam's schematics were a necessity to laying out this design. The Vector "slit-n

-wrap" tool P 180 also is considered a necessity. It would have taken an order of magnitude longer to construct the memory board without the tool. The greatest single expense was the 2124L Rams. A dynamic Ram board would be cheaper and use less real estate.

The 24K memory now on line has allowed me to load really big utilities to support software developement.

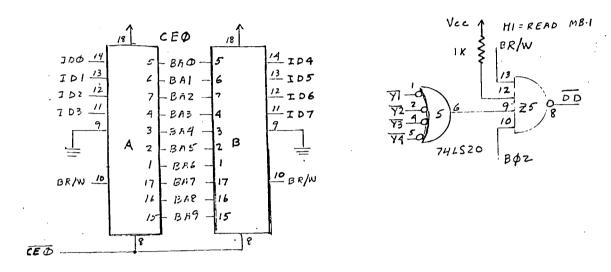
Not shown on figure 2 are the .01 mfd. bypass caps or 50 mfd., 16v electrolytics. Use one bypass per 1K memory and 4

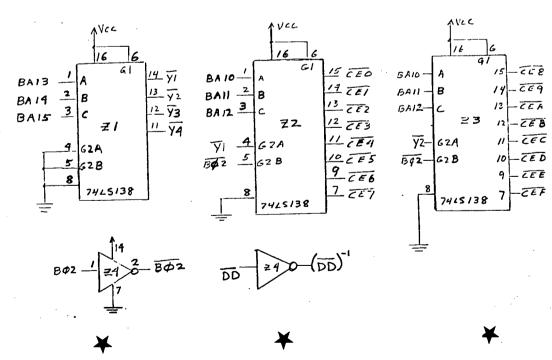
MEMORY BOARD #1.1

to 8 filter caps depending on board size and shape.

Board layout should minimize long traces provide for modest expansion in case of design change and adequate power capability.

The next design will be bigger and better I/O board. 5 months of reliable operation is all the proof I need that this project can be expanded.





## **LETTERS**

ED:

This tip may be of value to ClP users who have bought or intend to buy the SARGON II program from Hayden.

I had a great deal of problems getting my copy to load. I kept getting a checksum error at the line starting at 1E00. In fact, I could only get one or two successful loads out of six. I'd rewind and run, but it always hung up at the same spot.

I returned it for another copy. Same problem, same spot. I tried another recorder. Ditto.

Since Hayden would not give out a code listing so that I could make the correction manually, I found another solution.

Using the Aardvark 'Peek-a-Port' utility:

10 A=61440:B=A+1 20 WAIT A,1:PRINT CHR\$ (PEEK(B));:GOTO20

I could read the line with the error, find the faulty bit (at 1EOB), correct the program manually and make a copy. With a CEGMON ROM, this part was easy.

Also, since I have modified my C1P to save and load at 1200 baud, the program loads in about four minutes, rather than fifteen, a real time sver for those of us who don't have disks. Anyone else have this problem?

Gary E. Wolf Clifton, NJ

\* \* \* \* \* ED:

Has anybody out there interfaced a bar code wand to an OSI machine? (See BYTE April 1980.) I seem to recall reading about it in an old Peek (65), but could not find it in the back issues. We have green folding money for bright lad who's whipped this problem.

Daniel C. Smith DCS Software Products

\* \* \* \* \*

ED:

Dale Mayers has pointed out that my garbage collector failure demonstration program (March '81 Peek) does not fail. This is probably the only case on record where a typographical error has made a program designed to fail, run properly. Lines 40 and 50 should be interchanged. The following program:

10 DIM Z\$(11) 20 Z\$(1)="I LIKE" 30 Z\$(2)=" MICRO" 40 PRINT FRE(8) 50 Z\$(3)=Z\$(1)+Z\$(2) 60 PRINTZ\$(3)

When run on OSI ROM BASIC will print "I LIKE MICRO" followed by a long string of nonsense characters. The M. Minasi dimensioning trick does prevent the garbage collector from crashing. However, subscripted string variables can be garbled. The bug is very fickle. It can be affected by memory size, program position and string length. For example, the following program will run correctly for a three letter input such as "BUG" but garble strings if "PEEK" is input.

10 INPUT A\$
20 DIM A\$(11)
30 FOR X=0 TO 11
40 A\$(X)=A\$
50 NEXT
60 PRINT FRE(8)
70 FOR X=0 TO 11
80 PRINTX;A\$(X)
90 NEXT
0 BUG 0 PEEK

1 PEEK

7 BUG 7 \$\$\$\$ 8 RIG 8 \$\$\$\$

1 BUG

8 BUG 8 \$\$\$\$\$\$\$\$\$\$\$\$\$PEEK PEEKPEEKPEEKPEEKPEE

9 BUG 9 PEEK 10 BUG 10 \$\$\$\$

By the way, if the improved machine code from the June Peek is installed, Dale Mayer's program will run correctly.

5 DIMA\$(5)
7 A\$(0)="WET"
10 A\$(1)="I LIKE"
20 A\$(3)="PEEK(65)
25 A\$(3)="TO READ "
30 A\$(2)=A\$(1)+A\$(3)
40 PRINTFE(8)
50 PRINTA\$(2)
60 PRINTA\$(3)
OK
RUN
23602
I LIKE TO READ
TO READ



RE: Boolean Contest

Suppose you need a subroutine to produce a random number between 1 and 4, but you wish to control the probability differently for each number. For example, I desire "1" 60% of the time "2" 20% "3" 15% and "4" 5% of the time. The subroutine at line 102 will do this.

Listed below is a program to call the subroutine 2000 times and print out the actual occurance of each digit.

10 FORX=1TO2000:GOSUB100:A(K)=
 A(K)+1:NEXT
50 FORX=1TO4
60 PRINTX,A(X)/20
70 NEXT
99 END
100 N=RND(9)
102 K=-1+(N<.4)+(N<.2)+(N<.05)
104 K=-K:RETURN
OK

RUN

1 59.7 2 21.75 3 14.45

The only place I have seen such a construction used was in OSI's "Star Trek" to determine the number of Klingons in each quandrant.

130 k=K+(N<X2)+(N<Y2)+(N<.28)+
(N<.08)+(N<.03+(N<.01):K9=
K9-K:GOTO 160

RE: Question by Jay Jackson regarding number of nulls after Cr.

The number of nulls is

controlled by a routine in the system monitor. For the 2P the code is:

\$FF7B LDX #\$0A decimal 10 \$FF7D LDA #\$ 00 null \$FF7F JSR \$BF15 output SFF82 DEX \$FF83 BNE \$FF7F again ?

The code for the 1P is similar. The number 10 (\$0A) is fixed unless you replace the system monitor with EPROM. Since the output vector in the IP is in RAM, you can move the routine \$FF69 - FF8A into RAM and point the output vector to it. You are then free to change the LDX #\$0A operation to any number of nulls wanted.

RE: Letter by Pete Hitt regarding replacing character generator ROM

The stock OSI character generator is a 2316 ROM. The March issue, page 6 of PEEK (65) explains the two pin correction which must be made. Since the character generator is always enabled, these are D.C. levels. For OSI's ROM D.C. levels. For OSI's ROM pins 18 and 20 are connected to +5 volts. If you want to install a 2716 EPROM with custom characters, pins 18 and 20 must be grounded. Some OSI boards even have a jumper provided to do this. Would you believe an OSI computer that outputs in Japanese??

E. Morris Midland, MI

C.D. Lombard of Olympia, WA, sends the following listing, which allows cursor positioning on the Cl or C4. Changing the values of L and H in line 10, allows your input

prompt to be positioned anywhere on the screen for whatever effect you desire.

10 L=0:H=212 15 GOSUB20:GOTO100 20 POKE9666,L:POKE9667,H: 25 POKE9674,L:POKE9675,H 30 POKE9682, L: POKE9683, H:

35 POKE9719,L:POKE9720,H 40 POKE9726, L: POKE9727, H: 45 POKE9733, L: POKE9734, H

50 POKE9745, L: POKE9746, H: 55 POKE9753,L:POKE9756,H 60 POKE9773, L: POKE9774, H

70 RETURN 100 FORI=1TO 32 PRINT" TEST

IT" NEXT 110 L=O H=215

120 GOSUB20

ED:

10 DATA2,5

20 READ VOLUME, NUMBER

30 RESTORE

40 IF SUBSSRIPTIONEXPIRY> NUMBER+6THEN80

50 INPUT"EXPIRY DATE"; SUB\$ 60 POKE PEEK(65), VAL(SUB\$)

70 SUBSCRIPTIONEXPIRY= SUBSCRIPTIONEXPIRY+12

80 GOSUB99

90 GOTO20

99 REM

I don't recall where I first heard of Mittendorf Engi-neering (905 Villa Nueva Dr., Litchfield Park, AZ 85340), but it was at least a year vear ago, and I'm surprised that I've read nothing about them This is a travesty since. which I will make my life's work, for the next few minutes, to correct.

First of all, they are fast. Up here in the Frozen North where the post office measures time in months (it's now quarter to June), I received what must bе instant turn-around time on my questions and/or comments. That brings me to point number two: every response is detailed, knowledgeable, informative, and typed for heaven's No scribbles on the sake! bottom of your own letter, but a personal response! And they answer every question, no matter how (retrospectively) no simple. And that was before I ordered anything.

Their catalogue is comparable to the old Progressive Computing and Aardvark (before the Journal came out) catalogues... sprinkled with tips and advice. But rather than specializing in programs, ME has an excellent line of peripheral boards. I bought their High Resolution Graphics Board and Music Synthesizer (not just sound generator) Board; the quality is virtually OEM and the docugenerator) documentation! Oh, the documen-tation!! Written in English, with foolproof insructions and illustrations, examples of use, and periodic updates. Clear, concise, and obviously well thought out.

I was particularly impressed by the fact that the by peripherals could be purchased in any configuration. couldn't afford \$185 for High Resolution Graphics Board Complete, (and didn't find out the cost of a complete kit), so I got the bare board and 43 pages of documentation for \$30. Modifications to my ClP

### SOFTWARE FOR OHIO SCIENTIFIC 4

VIDEO EDITOR

Video Editor is a powerful full screen editor for disk-based OSI systems with the polled keyboard (except C1P). Allows full cursor-control with insertion, deletion and duplication of source for BASIC or OSI's Assembler-/Editor. Unlike versions written in BASIC, this machine-code editor is co-resident with BASIC (or the Assembler), autoloading into the highest three pages of RAM upon boot. Video Editor also provides single-keystroke control of sound, screen format, color and background color. Eight-inch or mini disk: \$14.95. Specify amount of RAM.

SOFT FRONT PANEL

Soft Front Panel is a software single-stepper, slow-stepper, and debugger-emulator that permits easy development of 6502 machine code. SFP is a fantastic monitor, simultaneously displaying all registers, flags, the stack and more. Address traps, opcode traps, traps on memory content and on port and stack activity are all supported. This is for disk systems with polled keyboard and color (b&w monitor ok). Uses sound and color capabilities of OSI C2/C4/C8 systems (not for C1P). Eight-inch or mini disk \$24.95. Specify amount of RAM. Manual only, \$4.95 (May be later credited toward software purchase). Six page brochure available free upon request. TERMINAL CONTROL PROGRAM

OSI-TCP is a sophisticated Terminal Control Program for editing OS-65D3 files, and for uploading and downloading these files to other computers through the CPU board's serial port on OSI C2, C4 and C8 disk-based systems with polled keyboards. Thirteen editor commands allow full editing of files, including commands for sending any text out the terminal port and saving whatever text comes back. INDUTL utility included for converting between BASIC source and TCP file text. Eight-Inch or mini disk \$39.95. Manual only, \$2.95.

OSI-FOURTH 2.0 / FIG-FORTH 1.1

OSI-FORTH 2.0 is a full implementation of the FORTH Interest Group FORTH, for disk-based OSI systems (C1, C2, C3, C4, C8). Running under OS65D3, it includes a resident text editor and 6502 assembler. Over one hundred pages of documentation and a handy reference card are provided. Requires 24K (20K C1P). Eight-inch or mini disk \$79.95. Manual only, \$9.95. "OSI-FORTH Letters" software support newsletter \$4.00/year.

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were minimal, and involved a few jumpers using existing sockets and plated-through holes. The boards themselves were easy to put together, even easier to connect, and don't disrupt normal computing activities. I won't even try to describe the results... they are as described in the literature, and up to the quality of everything else already mentioned.

To make a long story even longer, I am most definitely IMPRESSED with the service and the products offered by Mittendorf; I have no hesby itation in urging other ClP'ers to expand their ClP'ers to expand their horizons, and in fact, I'm a little surprised that this company has so far been "undiscovered". It's really a shame, because the potential is there, and they are more than eager to please. RETURN

John Kula Victoria, Canada

John:

For me... 10 compose ad for Peek (65) 20 send it in

30 hire more order-takers!

\* \* \* \* \*

ED:

To Jay Jackson, who wanted to know how to get rid of the 10 nulls at the end of each line when he uses a Model 33 TTY:

Those nulls are put there by write-to-cassette-port code so that when a SAVEd tape is re-LOADed the system has time at the end of each line to process the input. To get rid of the nulls, you need to usurp the write-to-cassette -port function. Here is a short program that will suffice.

48 NONULL PHA save the character at location \$0222, in the 'dead' area not used by OSI.
AND #\$02 drop all 29 02 but out status F0 F9 BEQ LOOP not ready, try again PLA 68 get the character STA \$F001 write it 8D 01 F0 to port JMP \$FF69 and go to 4C 69 FF regular code

This code can be located anywhere in memory since it

has no hard-coded addresses in it. Locations \$021A-B must be changed to point to the beginning of the routine, low address byte first, then high byte. One good location to place it would be at location \$0222, in the 'dead' area not used by OSI.

The operation sequence will he\*

- 1) Enter the program in memory (perhaps using the monitor, or a BASIC program and POKEs). This need only be done once.
- 2) Cold start BASIC.
- BREAK and to the monitor.
- 4) Enter '.021A/22<02. 0000G' where '<' represents a carriage return. This assumes the routine is at \$0222; if not, change the '22<02' to the correct address. This sequence of commands changes the output vector at \$021A-B to point to the new routine and then performs a warm start of BASIC.
- 5) You do not have to SAVE to start sending output to the TTY, this should occur immediately. If you need a delay at the end of each output line (most TTYs will, I think) use the NULL command.

Good luck!

++hobbitt \* \* \* \* \* ED.

- 10 REM MICROCOMPUTER APPLICATIONS, BOX 2914, ABILENE, TX., 79604 15 REM NAME OF THIS PROGRAM IS
- 'FILLOC'
- 20 REM THIS PROGRAM WILL CALCULATE THE FILES THAT ARE LOCKED
- 21 REM AND WRITE THEM ON THE SCREEN
- 22 REM THE NUMBERS THUS PRESENTED NEED TO BE UNLOCKED BY
- 23 REM THE USE OF 'WAITCLEAR N' FOR EACH NUMBER SHOWN
- 30 PRINT"THE FOLLOWING NUMBERED RESOURCES ARE LOCKED"
- 40 FORN=1 TO255
- 50 IF (PEEK (55333+N/8) AND2 (N-INT(N/8) \*8))>OTHENGOTO70
- 65 PRINTN
- 70 NEXTN
- 80 PRINT:PRINT:PRINT:PRINT
- 90 END

This formula comes from page 8-21 of OSI multiple user computer systems manual (c) OSI 1978. In my manual there was a typographical error which I could not fathom until

I searched through several of the OSI 65U utility programs which gave a "DIRECTORY BUSY" message. I found a similar formula and filled in the blanks as above.

It works for me in the level 3 mode when I lock up a terminal while debugging; just RUN"FILLOC" on another terminal and clear it up!

Verle Byars Abilene, TX

P.S. I use a CIIIB (74) with two terminals, soon to add two more.

Verle:

This routine should be used with great caution as it clears, ALL resource locks, even those put in place by another user.

\* \* \* \* \*

ED:

In response to Jim Sanders, Peek (65), June 1981.

The selection of the scratch pad RAM is done by causing RAMCE to be High (for those of you who have a Sams Manual and like those things). Setting RAMCE high is accomplished by setting the signal RS low. RS is PA4 of the PIA on the 510 board. This can be done (refer to Assembler Programmers Guide to OSI board Interfacing published in Peek (65)) by the following:

continued

### MICRO DATASTAT'S UTI INTERFACE TO OS-65U Level III

- \* Allows a telephone user full control of 1 port (any) under OSI Timesharing Basic.
- Automatically answers the phone and connects to one partition of Level III, recycle to "Wait for phone to ring" when disconnected.
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POKE 63233,4 Select PORT A Data Direction Register POKE 63232,16 Set bit PA4 to Output

POKE 63233,0 Select PORT A Data Register POKE 63232,X Set PORT A bits (1=Hi,0=Lo)

You must be very careful with the last POKE because the

other bits on PORT A are used for software selection of the Z80, 6800 and 6502 and for partition selection.

Brian Hartson

\* \* \* \* \* ED:

A "gotcha" recently discovered: Anyone using the code below to obtain a true rubout (this was popular before the Tech note fix from OSI to give destructive backspace came out) will find that running NECDRV with the latest NECDRV fixes for left-hand margin will still result in loss of the left hand margin. To solve the problem, replace the code below with the OS-65U destructive backspace outlined in Tech note 28-8.

GOSUB5000 5000 POKE 21340,95:POKE1373,76: 5010 POKE1374,0:POKE1375,92 5040 READ MAC:POKEPTR,MAC:

5020 FORPTR=23552T023573 5040 READ MAC: POKEPTR: RETURN DATA 201,95,208,15,169,8,32,8, 40,72

DATA 169,32,32,8,40,104,76,76, 5,76,97,5

If O.G. Haywood (Peek (65), June '81) has a parallel Spinwriter, the problem with stopping at end of page in BASIC is solved by disabling the NECDRV paging function, e.g. POKE 25164,234:POKE25165, 234:POKE25166,234. Remember to SAVE"NECDRV" after the

William W. Gibbs Shenandoah Microcomuter Services, Inc. \* \* \* \* \* |

I have an OSI C4P cassette version and I would like to expand the memory from 8K. Do you or your readers have any information on the 527 memory expansion board? If so, will it plug directly into the OSI backplane without any modifi-cations? If it doesn't, do you have any other suggestions or diagrams on different ways to expand the memory?

John Frankforther Maumee, OH

John:

The 527 memory expansion board is an OSI standard 48 pin bus board having a maximum of 24K bytes of memory. The only problem that you might have is If your C4P has addressing. the 502 CPU board, then it has 8K of RAM on it beginning at \$0000 and ending at \$1FFF . The 527 board comes standard addressed from \$0000 to \$5FFF.

With some modifications to the 527 board or 502 board, you could move it to \$2000 to \$7FFF, giving you a total of 32K bytes of RAM. Probably the easiest to modify is the 527 board. To accomplish the modification, cut jumper W2 free from IC UF9, pin 13 and connect it to IC UF9 pin 10. This modification should put the 527 board at \$2000 to

Before doing this, you should purchase the Sams Photo-Fact Manual for the C4, catalog #TM200 and familiarize yourself with the 527 board part locations. Good Luck!

\* \* \* \* \*

ED:

I have just received Vol. 2, #5 of Peek (65), in which my letter concerning character generator code was published. I appologize that my reference to the "enabling logic" of the Char. generator ROM was not clearly understood. My first impulse was to provide a direct answer to your question but I believe that a more general response will give you and readers of Peek (65) a better understanding of the 2716/2316 EPROM/ROM Family. This information will be applicable the monitor ROM and BASIC-in-ROM chips as well as the character generator ROM. Let me state at the outset that I cannot lay claim to being one of the "smart guys" you refer to, but I know a couple of those "smart guys" and I have researched the subject enough to be able to build an EPROM programmer.

all of First understand a feature of these chips (and any other chip which connects to the data bus) called "tri-state logic". Since the CPU, buffers, ROM memory and RAM memory are all tied directly to the data bus there has to be a "traffic cop" to allow only one byte of

## DATA CONVERSION

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data on the bus at a time. Without special control all of these devices would be dmping data onto the bus at once and the results would be chaos. For purposes of this discussion "tri-state" means that pins of chips connected to the data bus can be:

- 1. AT 5 VOLTS (A LOGIC HIGH...A '1' BINARY BIT 2. AT 0 VOLTS (A LOGIC
- LOW....A '0' BINARY BIT 3. AT HIGH IMPEDANCE...NO
- COMMUNICATION AT ALL EITHER TO OR FROM THE DATA BUS

This third state, high impedance, is the 'traffic cop' and it is accomplished by controlling the logic state, either high or low, of certain other pins on the chip which are call "enable" or "chip-select" pins.

The 2713 ROM (read only memory) is identical to the 2716 EPROM (erasable, programmable, read only memory) with the following exceptions: 1) It cannot be erased, 2) It is programmable only by the manufacturer, 3) It is cheaper (if you buy them by the thousands) and 4) The chip-select logic (whether the chip-select pins must be high or low to enable the chip) can be specified by the customer. In fact, this last exception is the reason why everyone who has replaced the system monitor ROM with an EPROM must change some jumpers on their boards...the chip-select logic is different.

ED:

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5 to 10 times faster than Basic. Once you use it, you'll never go back to BASIC! source listing add		75.00 20.00
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The chip-select pins on the 2716 and 2713 are pins #18, #20, and #21 (my first letter is in error in identifying these pin numbers). They are called, respectively, CS2, CS1 and CS3. The monitor ROM and BASIC ROMS are address decoded, meaning that the address lines are routed through decoder chips and the resulting decoded signal(s) are connected to one or more of the chip-select pins so that the ROMS are enabled if and only if the proper address appears on the address bus. The character generator ROM is different in this respect in that it is constantly enabled. This causes no problems because its output pins are not connected to the data bus but instead to the video output. The CG ROM in my superboard has pins 18, 20 and 21 connected directly to +5 volts. This means that all three chip-select pins must be at the logic high state to enable the CG ROM. If the character generator were replaced by a programmed 2716 then the chip enabling logic would have to be changed by removing pins 18 and 20 from 5 volts and connecting them to ground (0 volts). Pin 21 would be left connected to wollts. This would hold the new EPROM character generator constantly in the "READ" (or enabled) condition.

I hope that this explanation (by no means complete) will give you a better understanding about how these ROMs and EPROMs work and encourage some of you who would like to play around with the system firmware.

I would be happy to try to answer any questions which might arise, but please include a S.A.S.E.. Sometimes it seems that if the IRS doesn't break me, the post office will!

Pete Hitt P.O. Box 266 La Luz, NM

Pete:

Thanks for the lesson and clarification. Why not write an article for Peek (65) on your adventures with your EPROM programmer?

Αī

\* \* \* \* \* ED:

I have enjoyed Peek (65) and the latest comments on information networks and electronic mail. However, after perusing my last month's phone bill, my enthusiasm has dimmed somewhat.

A contrubuting factor to the length of my calls to your CBBS is my inability to get the editor part of your software to work right. It is, no doubt, my problem and not yours. However, using the control fuctions has never produced the intended result for me. In all probability, more information would help me a bit, but I feel the fundamental problem is using the phone line interactively.

Your comments and concerns about protocols are in order. I believe that for a service to be priced attractively, one must be able to compose off-line and then dial-up and send a message, or alternatively, request and download a message. Time on long distance should be used for transmission, primarily, a quick inquiry with a negative result should take less than a minute, (Nothing for me? OK, 'bye.) even several pages of text would take little time if uninterupted.

If the service were designed to used as a drop-off and pick-up point, then, a directory could be requested and down loaded first, then one would log off, to resume when choices are made and command lines are edited and ready. I realize this seems a step backwards, back to more of a batch orientation, but with computing power spread out, let's use our computers for processing and yours (or any central CBBS) as a central storage and retrieval point, not as an in-line editor for our messages!

This would also make better use of one scarce resource, your phone lines and ports. We would be able to offer more ports on our time share system here if some way of getting on and off the system easily existed. Optimimally, this would all be transparent to the user, but because of the intervention of the phone lines, one can't easily disconnect/reconnect.

Some simplified protocol for block transmission, perhaps similar to disk-file usage, would allow messages to be 'left' for someone, or, picked up. Actually, picking up messages is already more or less of a block transmission activity, and, once in the compose mode on the CBBS,

lines followed by carriage returns printed to the modem from a file should behave as if done from the keyboard. So perhaps it will not be so difficult after all, although setting up a system to respond to, say, search parameter requests, building and saving a response file, would require some design work.

By the way, you discussion of indirect files was welcome. I tried to use them when I first got my C8P, unfortunately, as I recall, they don't work on my video system between 65U and WP-2. This is all the more galling as the 65U's editor doesn't either! Any hints or thoughts about how to make them work would be appreciated by all us non C-3ers, for sure.

Ralph K. Requa Glendale, AZ

Ralph:

The present version of the 65U line editor uses the same RAM area as the 540 video driver board, so they are not compatible. I hear a new EDITOR located in a different area of RAM, is soon to be released.

Good ideas for the board. I'll get to work on them!

ΑL

\* \* \* \* \*

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ED

I am responding to Michael B. Carroll's letter on page 20 of issue #5, May 1981.

I suspect that his DD line is not being properly derived. Here is what I suggest: ED:

In response to Fred Schaeffer, Peek (65), June 1981.

multiple partition utilities is system utilities is a utilities package that is shipped with each multi user system by OSI, along with a manual. usually resides in the hard disk buffer, but once loaded, is relocatable. Contact your local dealer for a copy of the disk and manual.

Brain Hartson

\* \* \* \* \*

ED:

The most basic book for 6502 Assembly language is the MOS Programming Manual for MCS6500 Microcomputer Family. Not only does it cover all the instructions, but the Appendices hold a ready reference section for quick look-ups.

Shirley Emerson Kaneohe, ΗI

\* \* \* \* \*

This circuit enables the 8196 when R/W is high and address is \$DB01. DD is enabled low when R/W is high and 02 is high and the address is \$DB01.

Bruce Showalter Abilene, TX

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CONTINUED

A15 AH4 7412 743¢ A13 R/W A12 A11 A1Ø PA 7402

DD A8 A7 G2 A6 AR 7434 D7 8196 हा A1 Add 7404

ED:

We have some corrections to the article on modifying WP-3  $\,$ to support terminals other than the Hazeltine 1420. The locations we sent you were off by one in most cases - the result of misreading a dis-assembly. A correct table of keystroke locations follows:

ORRECT OCATION	PRINTED LOCATION	CURRENT VALUE
\$1979	\$1978	10
\$19DE	\$19DE	10
\$1A2B	\$1A2B	10
\$1975	\$1974	08
\$1989	\$1988	09
\$1985	\$1984	12
\$1981	\$1980	06
	\$1979 \$19DE \$1A2B \$1975 \$1989 \$1985	\$1979 \$1978 \$190E \$190E \$1A2B \$1A2B \$1975 \$1974 \$1989 \$1988 \$1985 \$1984

Our apologies.

Daniel C. Smith DCS Software Products

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way where they belong. spool D you don't have to have a lot of memory for indirect operations. You can do them within the 2K buffer of a disk file. Then you can input them to concatenate programs. you can do some really bizarre you can do some really blzarre things like capturing dialogues on disk for later printing. For those who want to timeshare "TTY43" makes your computer act like a terminal to the timeshare computer. A patch in DOS loads and unloads the disk loads and unloads the disk head after each IO which gives added disk protection when power fails, and it lets you in on the disk activity during those lulls, and for a little extra you can get a disk file word processor for your own personal needs which is easier to use than a typewriter. As the ad says it truly is a BIG BAG for the bucks.

Computer Power Fallston, MD



HEXDOS is a new disk operating system for the C1P MF, written by Steven P. Hendrix.

Peek (65) does not have a ClP, therefore, this product could not be tested. For more information contact Steven P. Hendrix, 415 S. Pierce, Enid, OK 73701.

### HEXDOS FEATURES

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- \* Includes an interactive disassembler and diskette formatter, and subroutines for random access disk files, written in BASIC. .
- \* Allows chaining of BASIC programs (one program can call another from disk and run it).
- \* Runs on the Challenger 1P MF, or on either the Challenger 1P or Superboard II with disk expansion.
- \* Programs available for use with HEXDOS include games (REVERSI, SURROUND, BACK-GAMMON, and FIFTEEN), a machine-language version of LIFE, a controller for the BSR home control system marketed by Radio Shack and Sears (uses software to generate ultrasonic signals to the control console).

## \* \* \* AD\$

65D Magic Boot: Adds renumber, full cursor editing, memory pack, screen clear and color background to BASIC. All routines are superfast machine code and handle All cases. These are not USR (x) routines, but extensions to BASIC itself. All you do is boot and these commands are ready to use--in immediate mode or in your programs. Also includes new BEXEC\* and disk utilities. \$50.00. UNIVERSAL SYSTEMS, 2020 West County Rd. B, MPIS., MN 55113.

65D Segs: Adds segmentation commands to BASIC. Allows segment calls (like GOSUB'S) to subroutines stored on disk. By nesting calls, LARGE programs may be written and will run in (24K) memory. (Requires Magic Boot) \$25.00. UNIVERSAL SYSTEMS, 2020 West Country Rd. B, MPIS., MN 55113.

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# PEEK (65)

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